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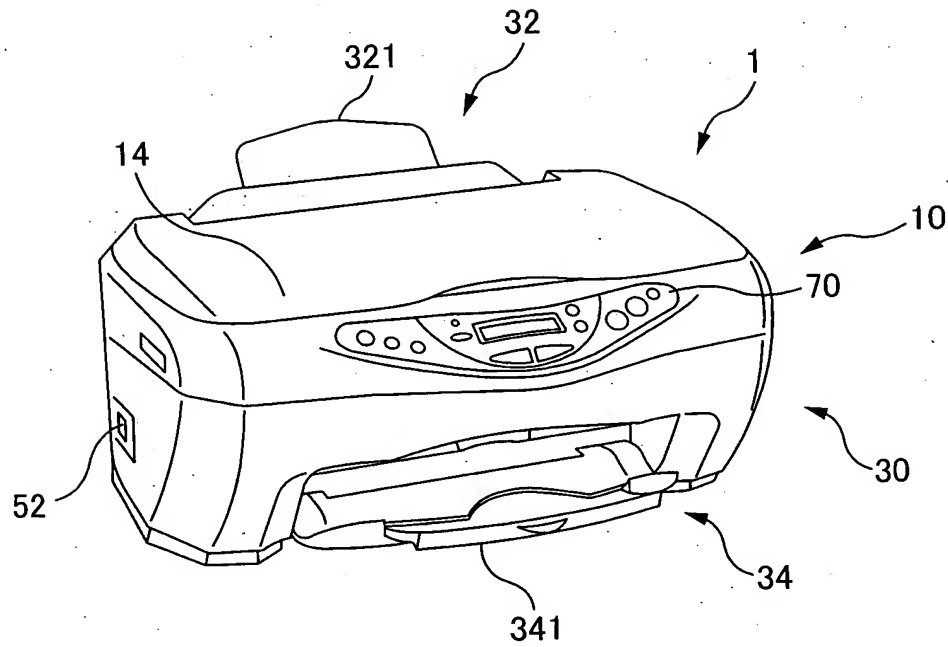


図1

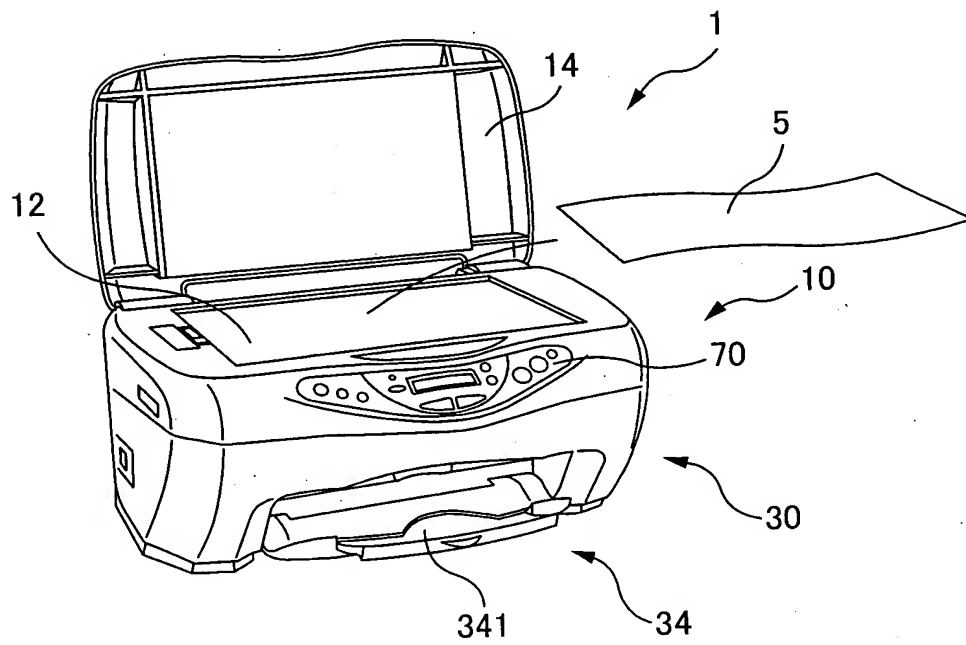


図2



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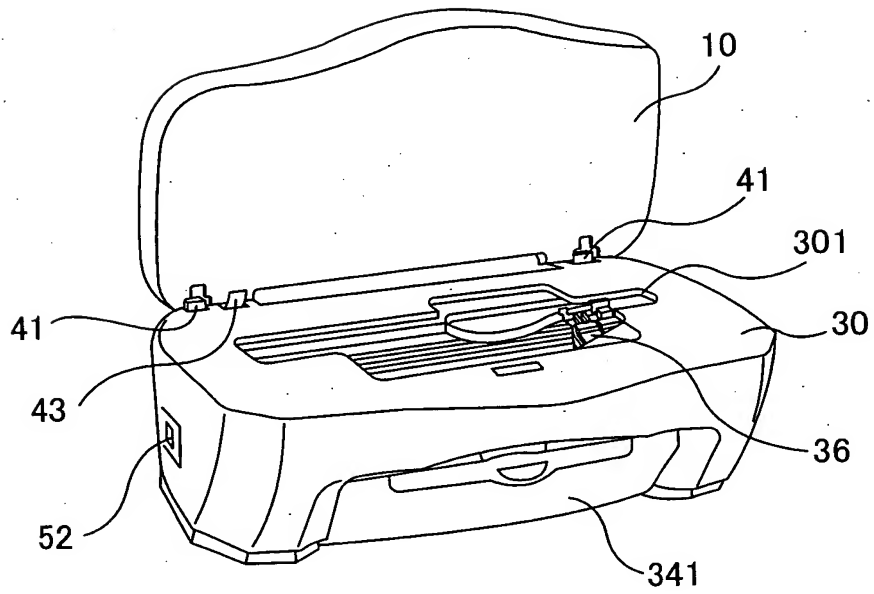


図4

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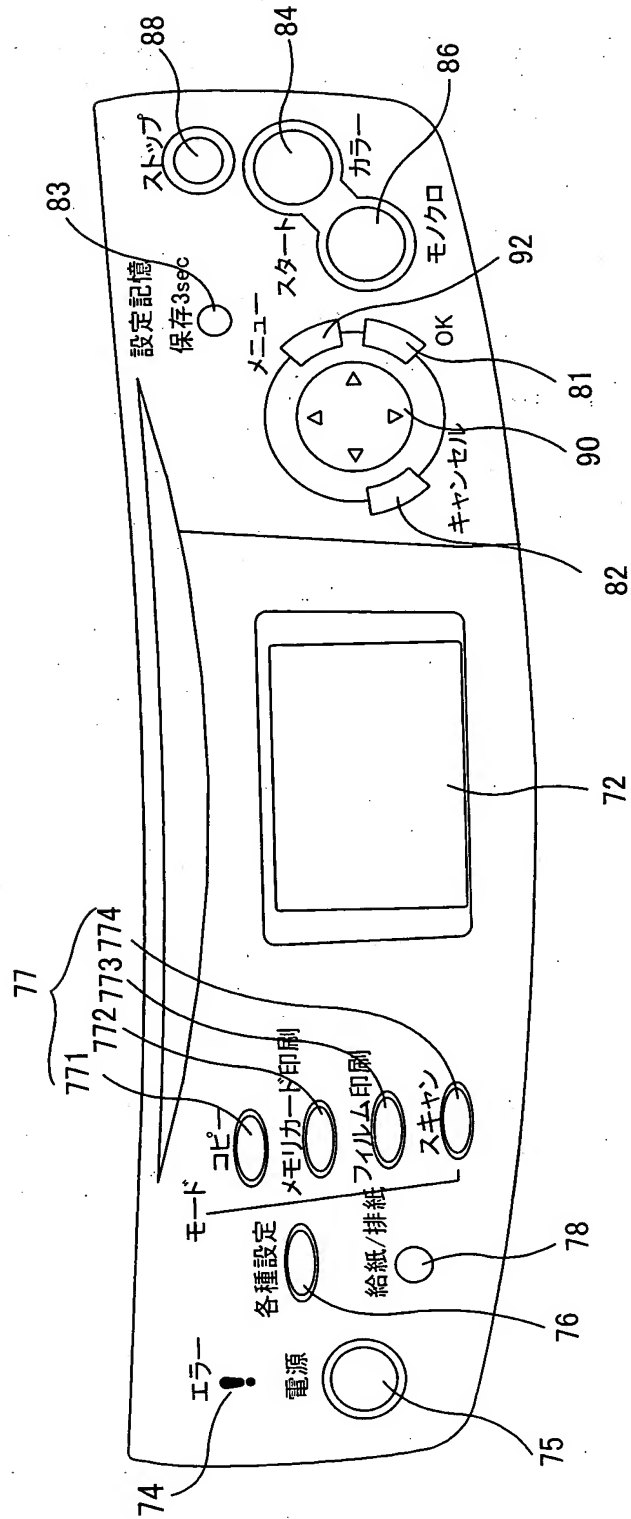


図5

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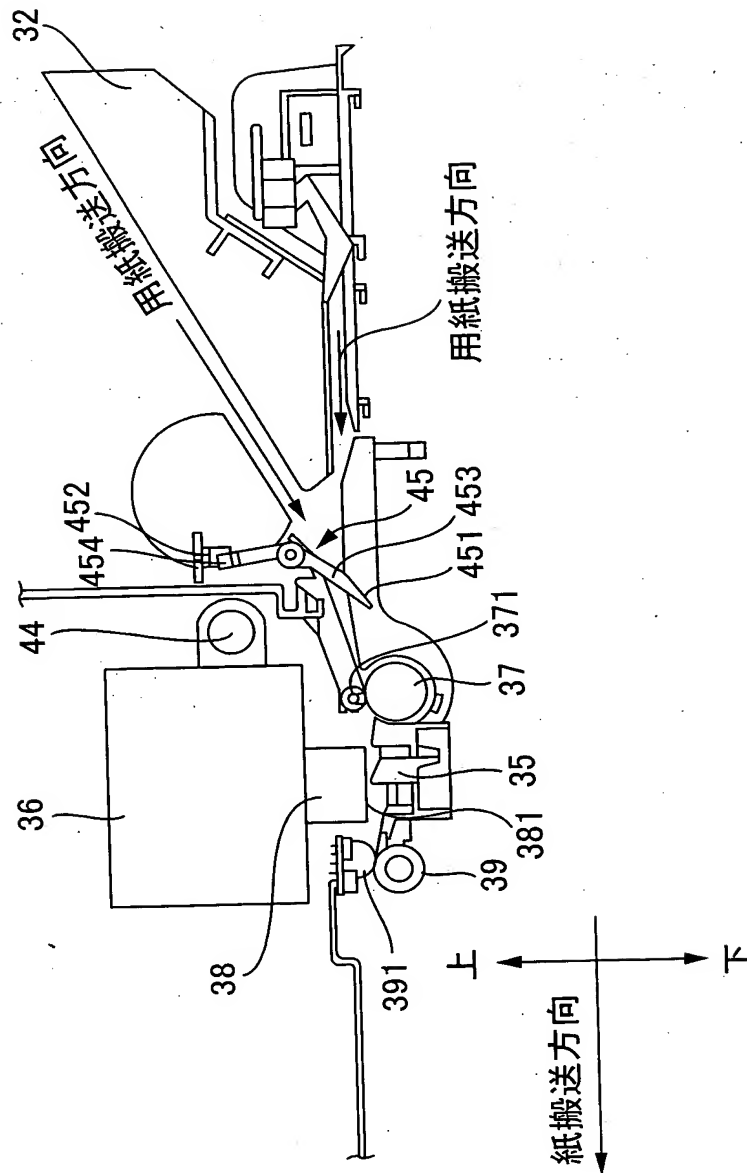


図6

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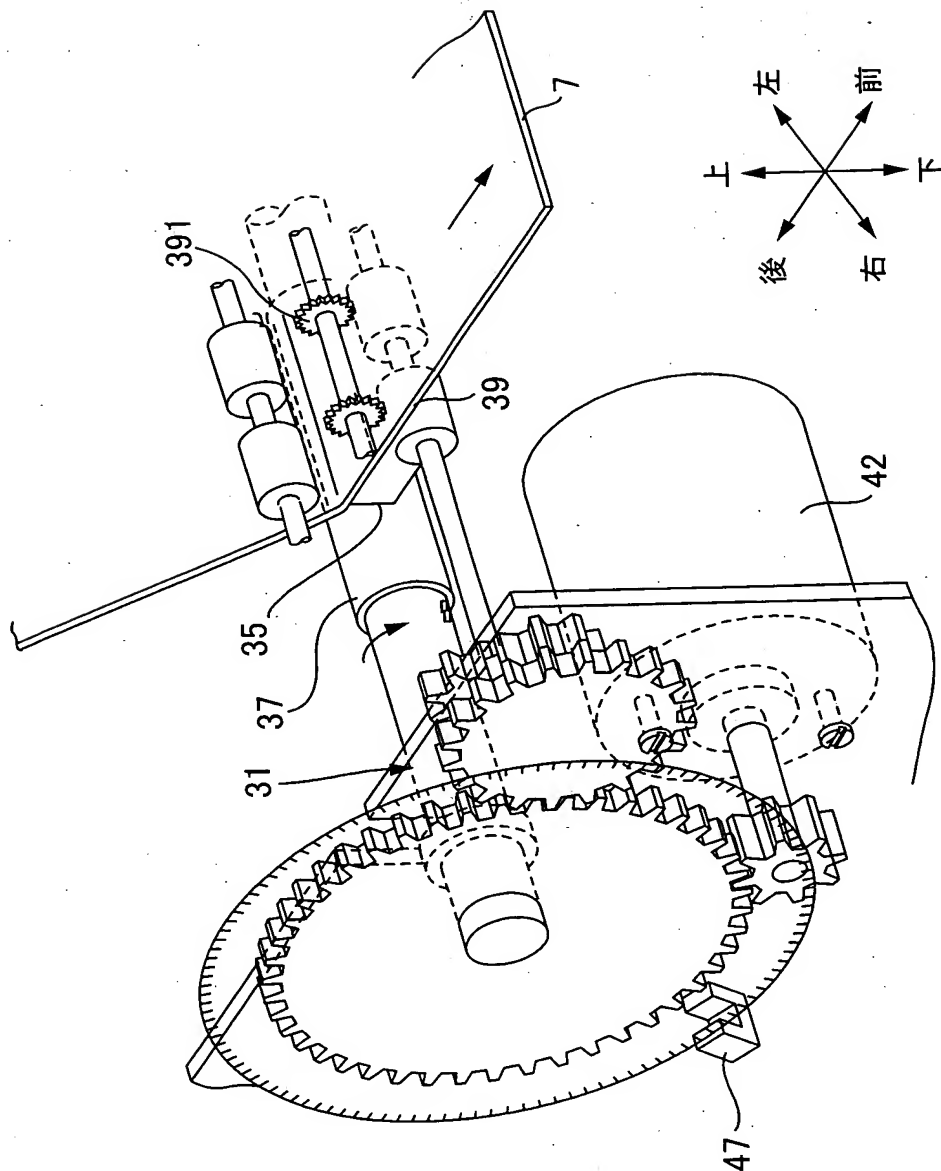


図7

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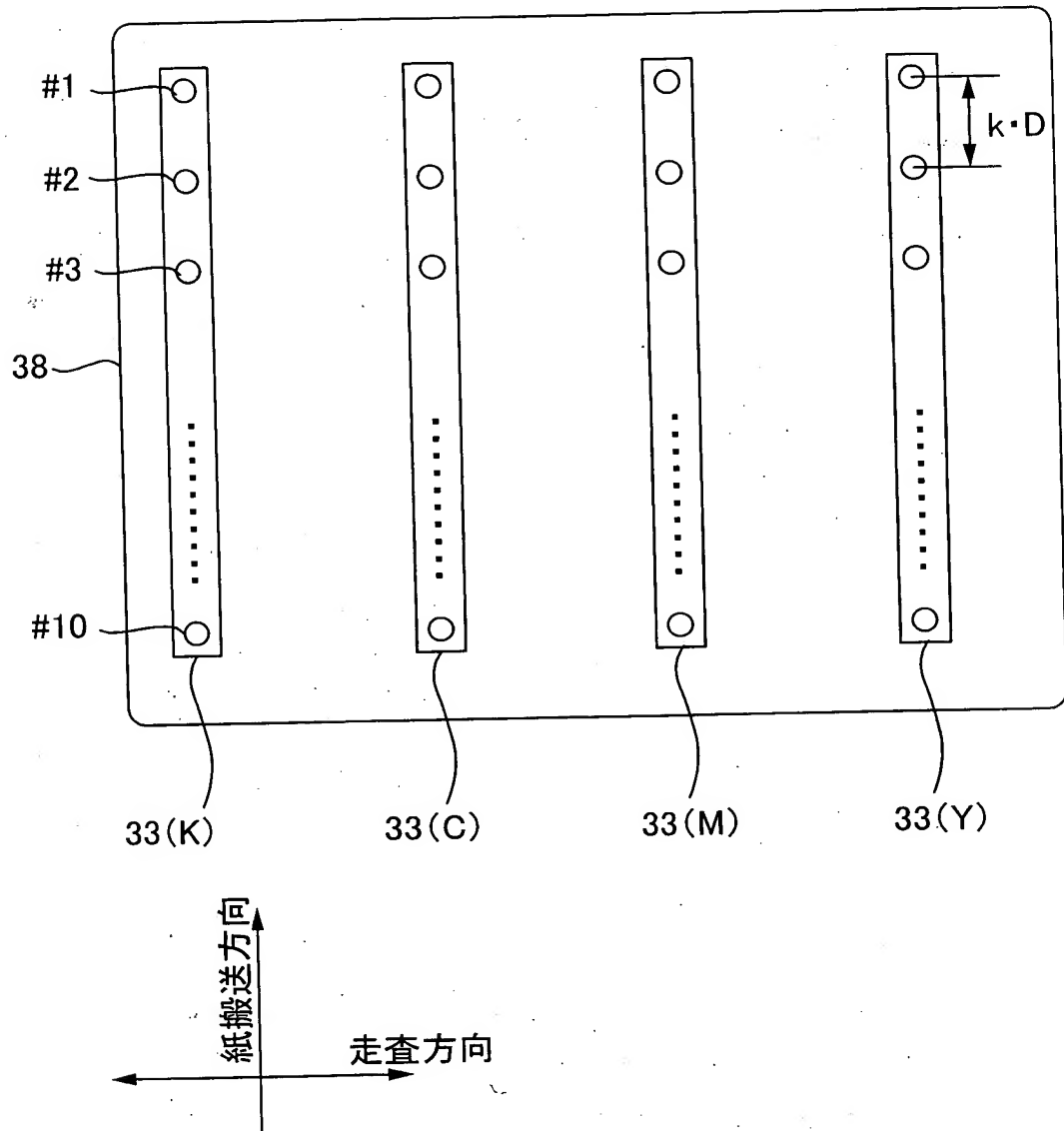


図8

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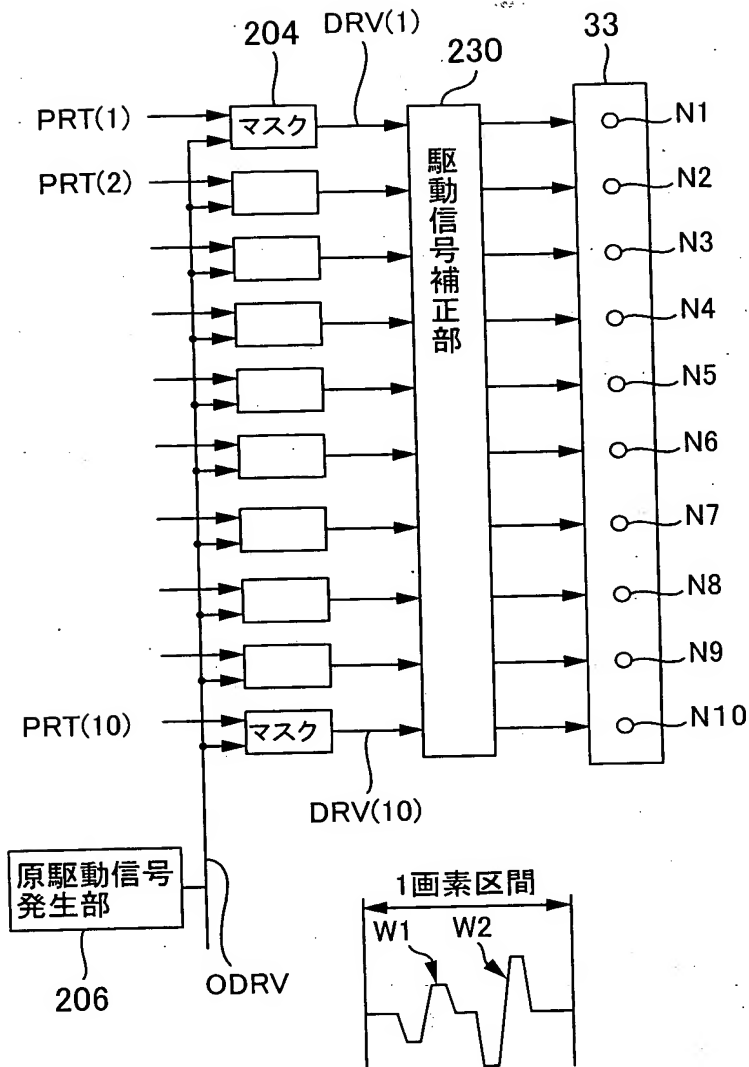


図9

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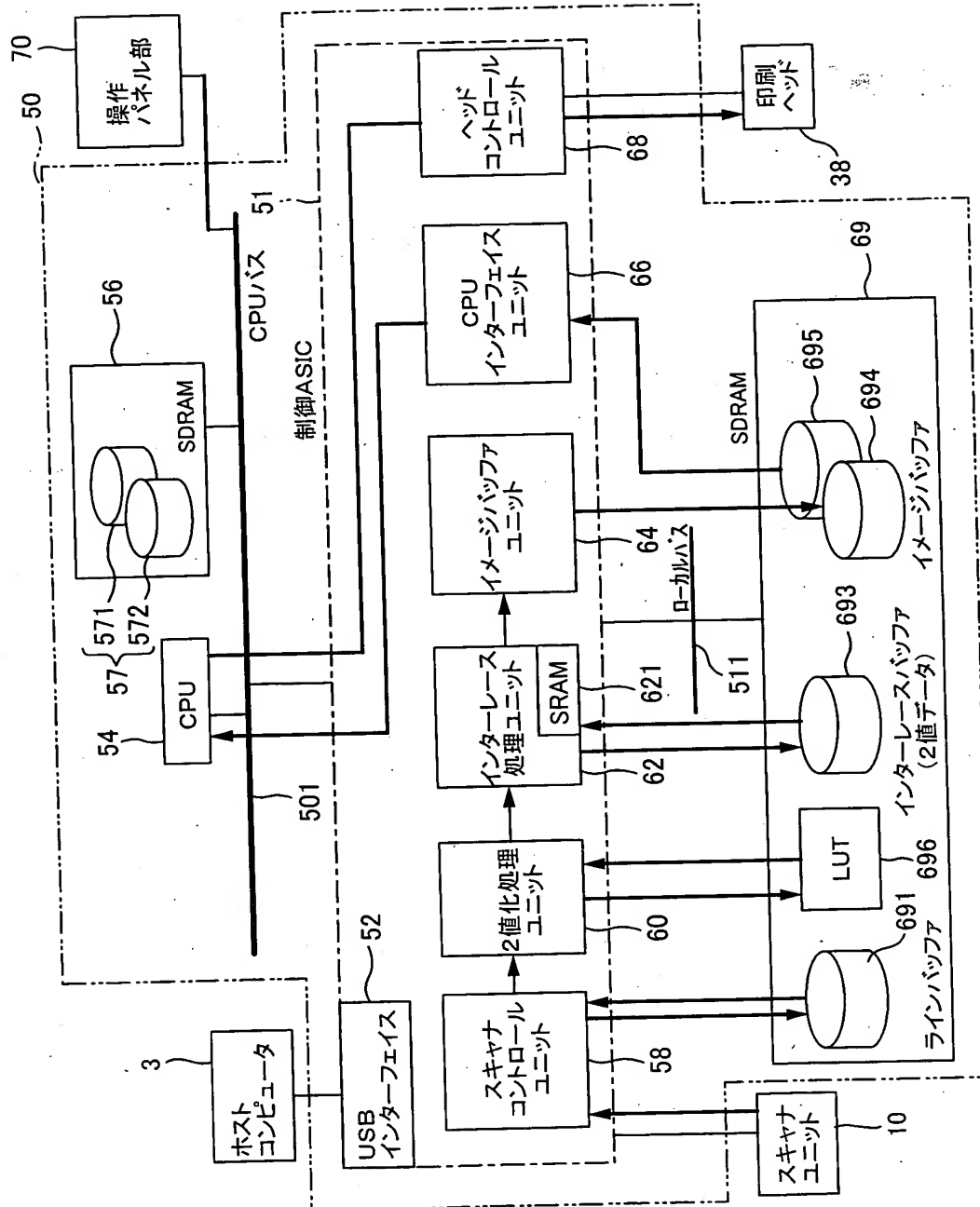


図10

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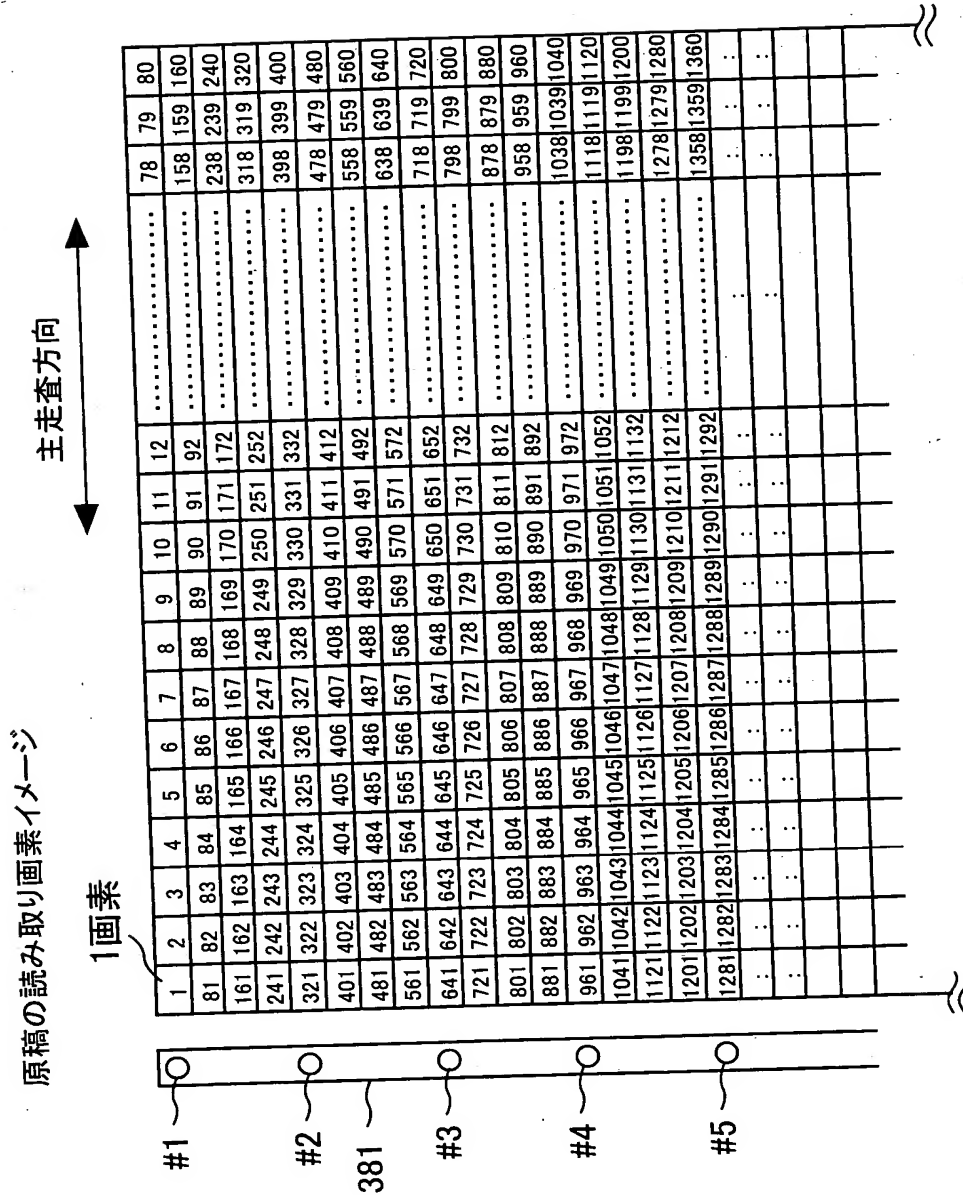


図11

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ラインバツファイメージ(画素イメージ)

1画素

B成分領域												G成分領域												R成分領域											
B321	B322	B323	B324	B325	B326	B327	B328	B329	B330	B331	B332	G321	G322	G323	G324	G325	G326	G327	G328	G329	G330	G331	G332	R321	R322	R323	R324	R325	R326	R327	R328	R329	R330	R331	R332
B241	B242	B243	B244	B245	B246	B247	B248	B249	B250	B251	B252	G241	G242	G243	G244	G245	G246	G247	G248	G249	G250	G251	G252	R241	R242	R243	R244	R245	R246	R247	R248	R249	R250	R251	R252
B161	B162	B163	B164	B165	B166	B167	B168	B169	B170	B171	B172	G161	G162	G163	G164	G165	G166	G167	G168	G169	G170	G171	G172	R161	R162	R163	R164	R165	R166	R167	R168	R169	R170	R171	R172
B81	B82	B83	B84	B85	B86	B87	B88	B89	B90	B91	B92	G81	G82	G83	G84	G85	G86	G87	G88	G89	G90	G91	G92	R81	R82	R83	R84	R85	R86	R87	R88	R89	R90	R91	R92
B1	B2	B3	B4	B5	B6	B7	B8	B9	B10	B11	B12	G1	G2	G3	G4	G5	G6	G7	G8	G9	G10	G11	G12	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10	R11	R12
B398	B399	B400	B398	B399	B400	B398	B399	B400	B398	B399	B400	G398	G399	G400	G398	G399	G400	G398	G399	G400	G398	G399	G400	R398	R399	R400	R398	R399	R400	R398	R399	R400	R398	R399	R400

図 12

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インターレースバッファイメージ
 (データブロックイメージ)

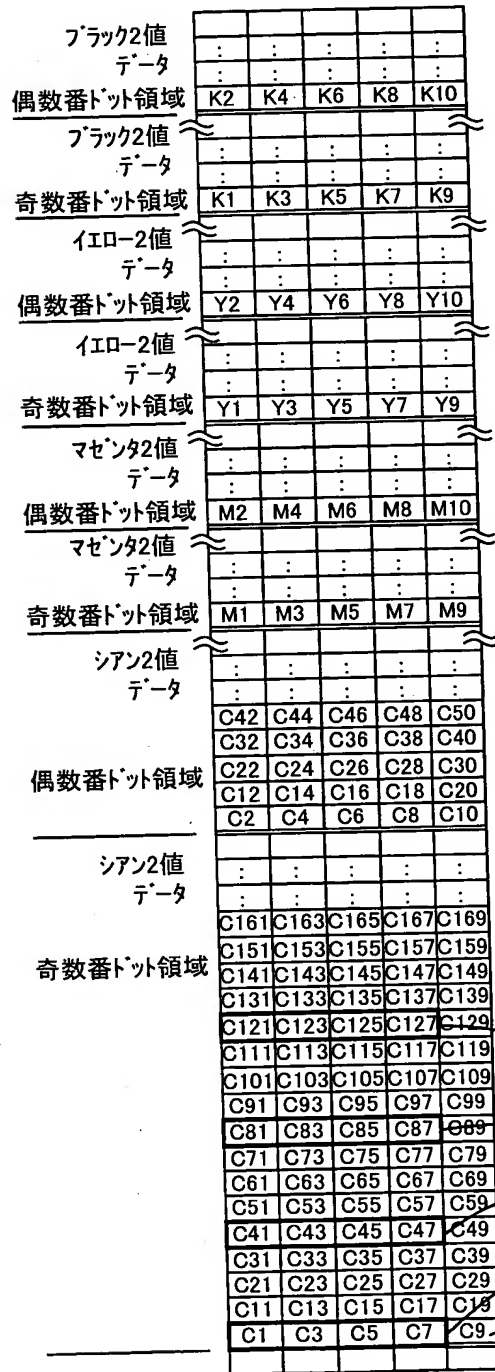


図13A

SRAMイメージ

C7	C47	C87	C127
C5	C45	C85	C125
C3	C43	C83	C123
C1	C41	C81	C121

図13D

レジスタイメージ

C1	C41	C81	C121
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図13C

SRAMイメージ

C121	C123	C125	C127
C81	C83	C85	C87
C41	C43	C45	C47
C1	C3	C5	C7

図13B

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ヘッド駆動データイメージ(データブロックイメージ)

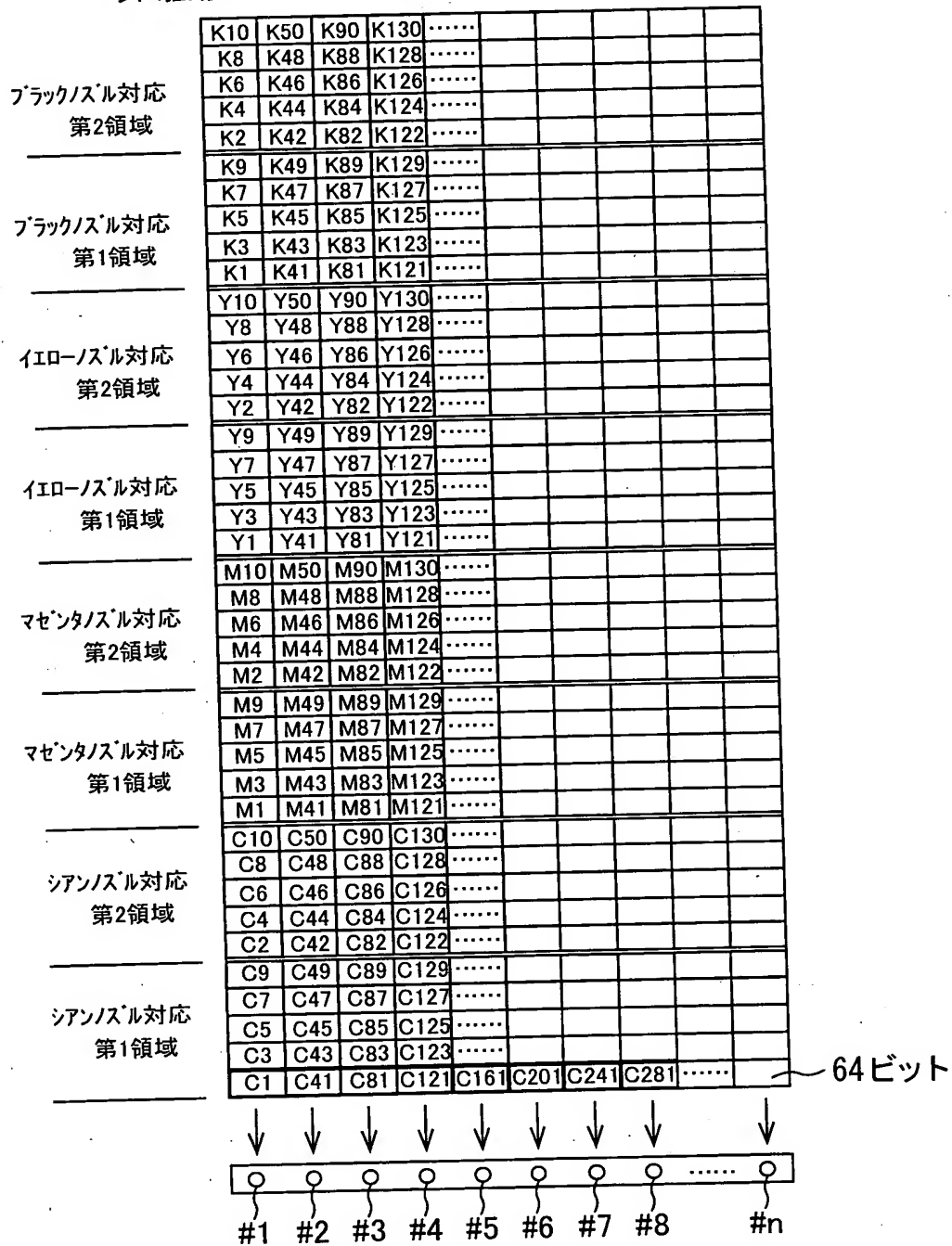
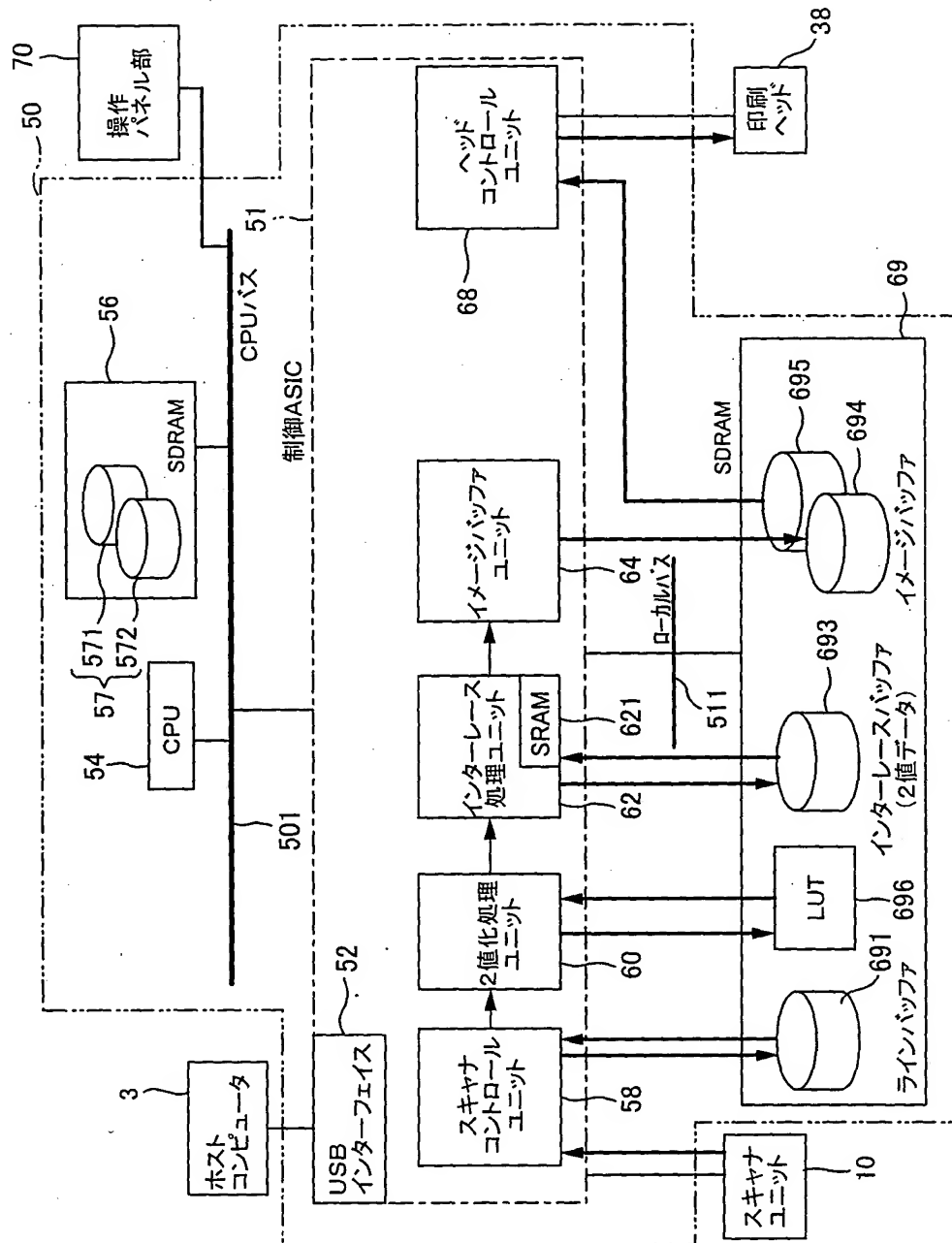
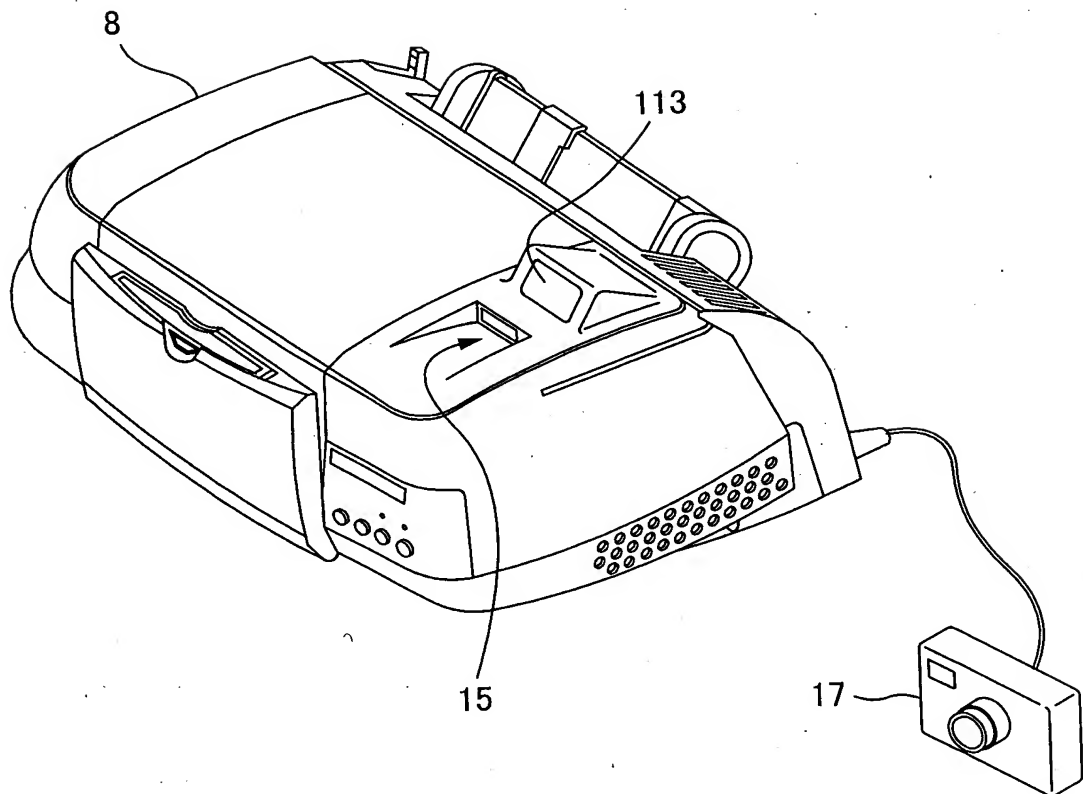


図14



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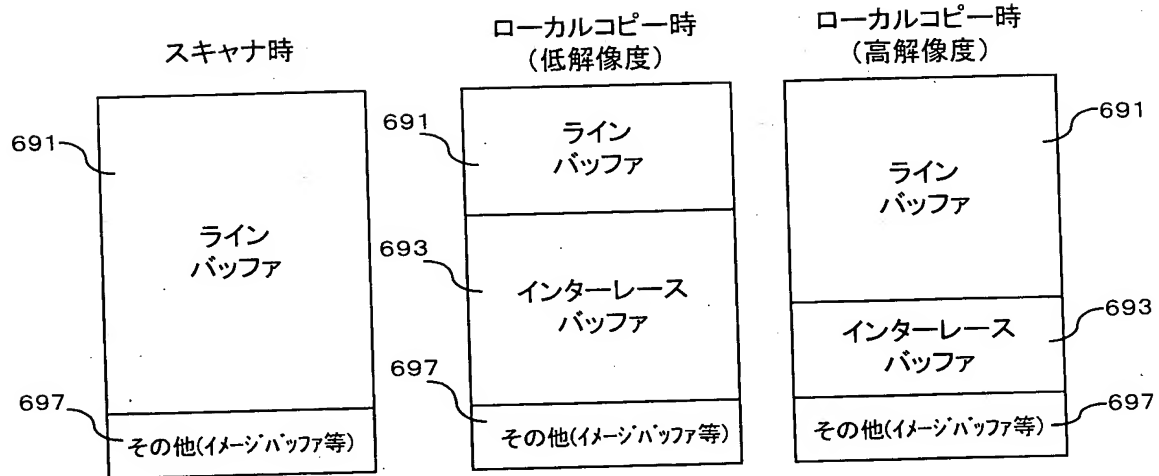


図17A

図17B

図17C

動作		割り当てアドレス		
		ラインバッファ	インターレースバッファ	その他
スキャナ時		0000h~CFFFh	なし	D000h~FFFFh
ローカル コピー時	低解像度	0000h~4FFFh	5000h~CFFFh	D000h~FFFFh
	高解像度	0000h~7FFFh	8000h~CFFFh	D000h~FFFFh

図18

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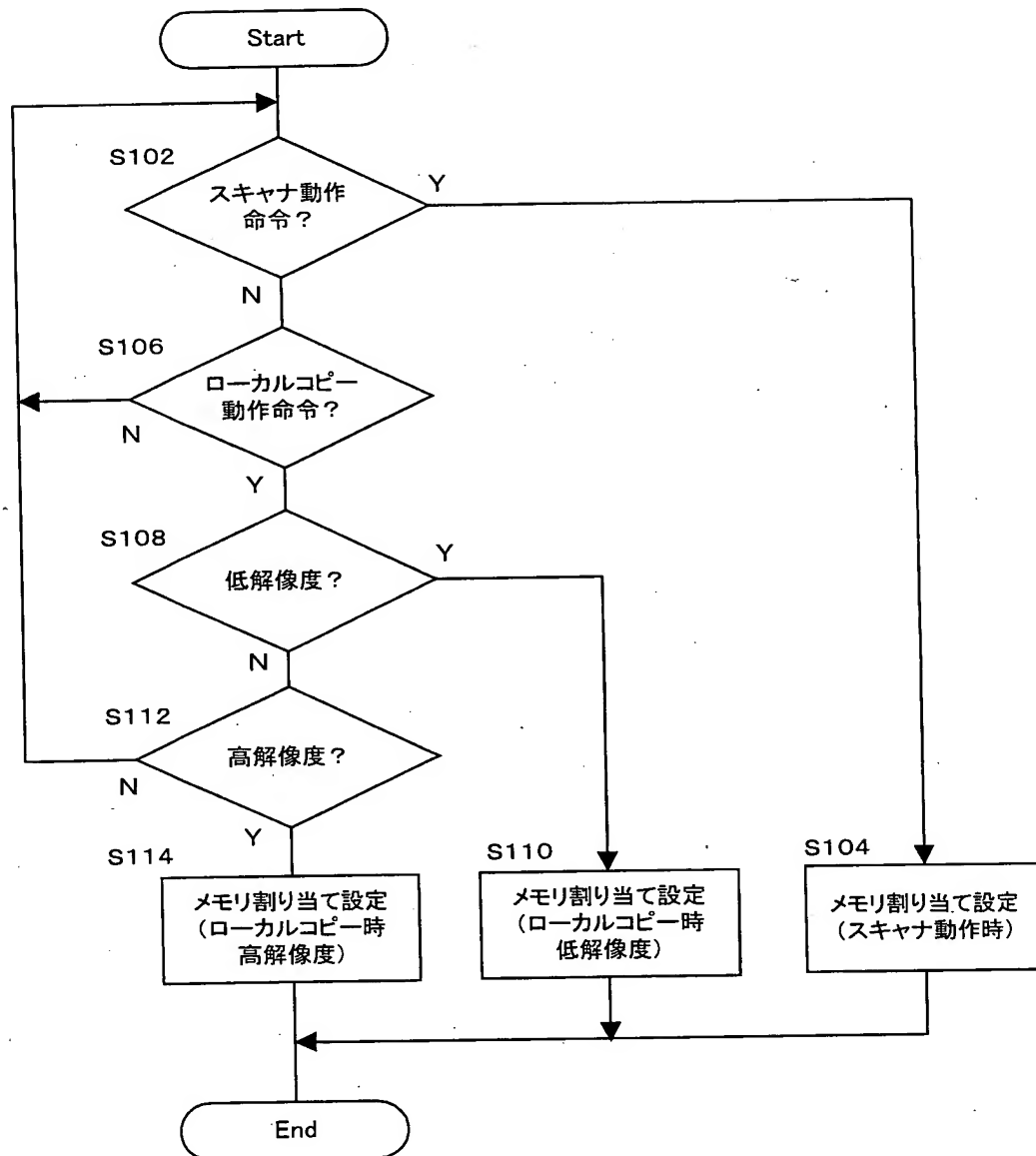
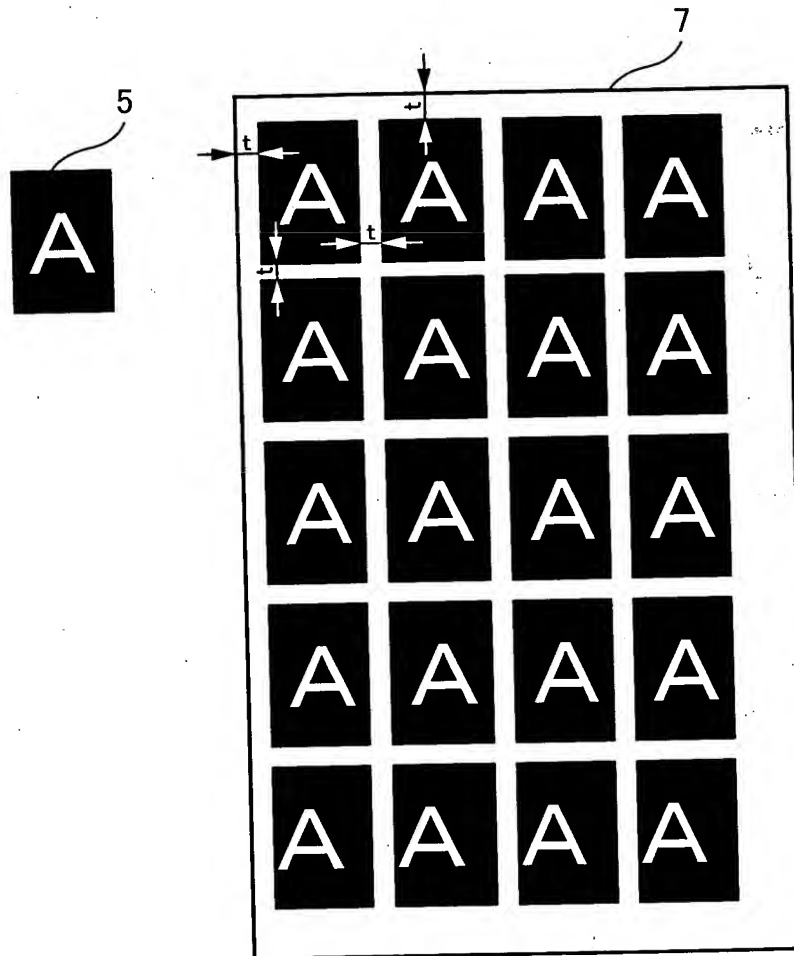


図19

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図21

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図22A

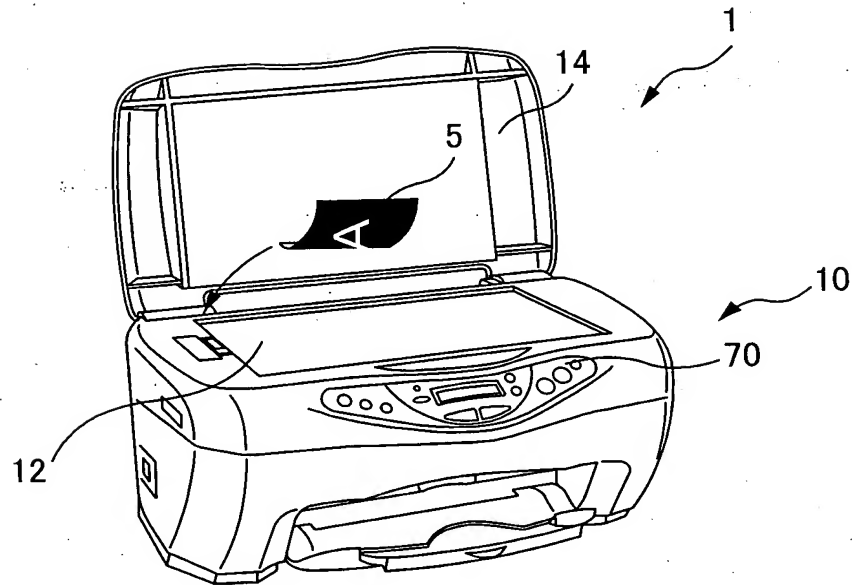


図22B

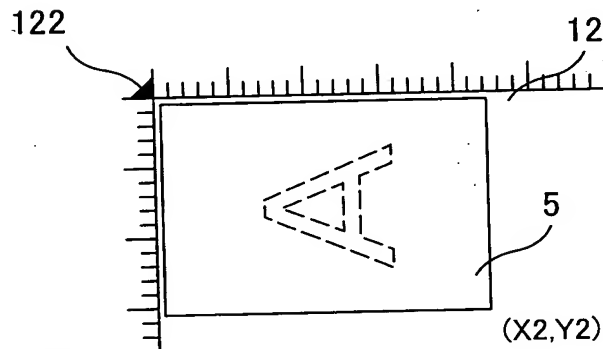
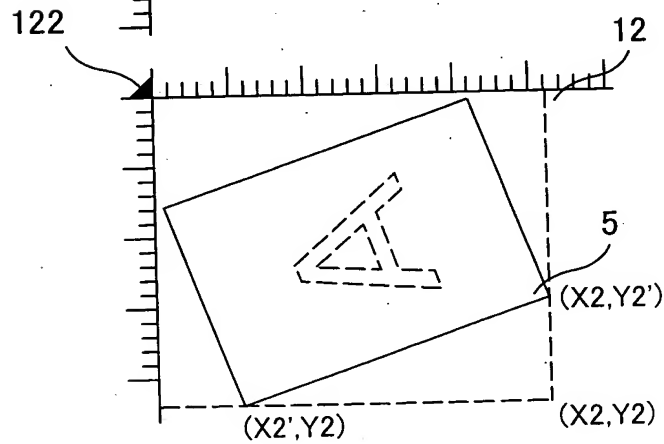


図22C



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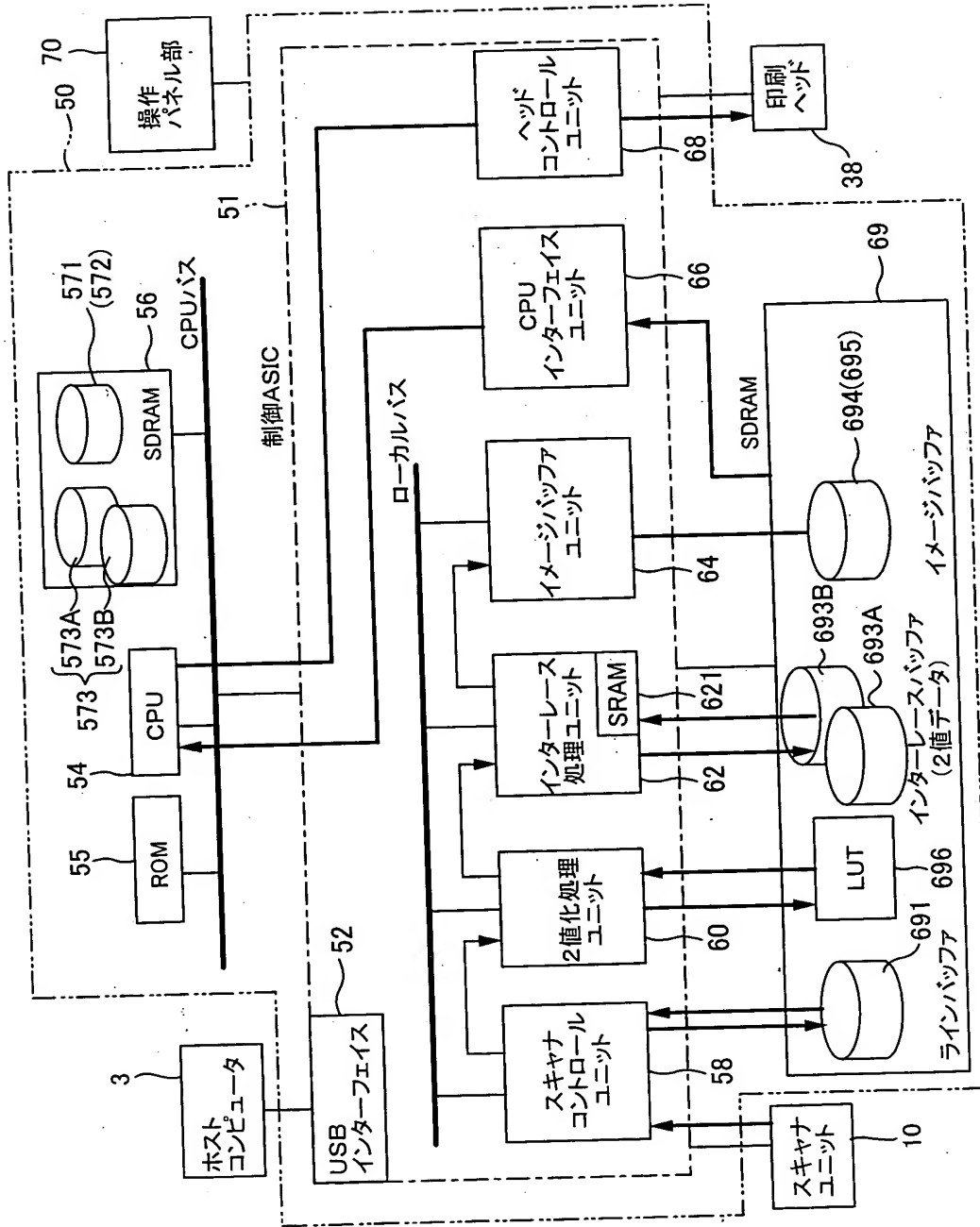


図23

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図24

図25A

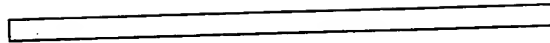


図25B

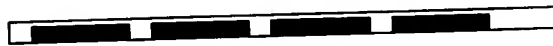


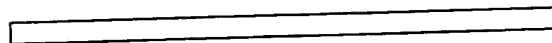
図25C



図25D



図25E



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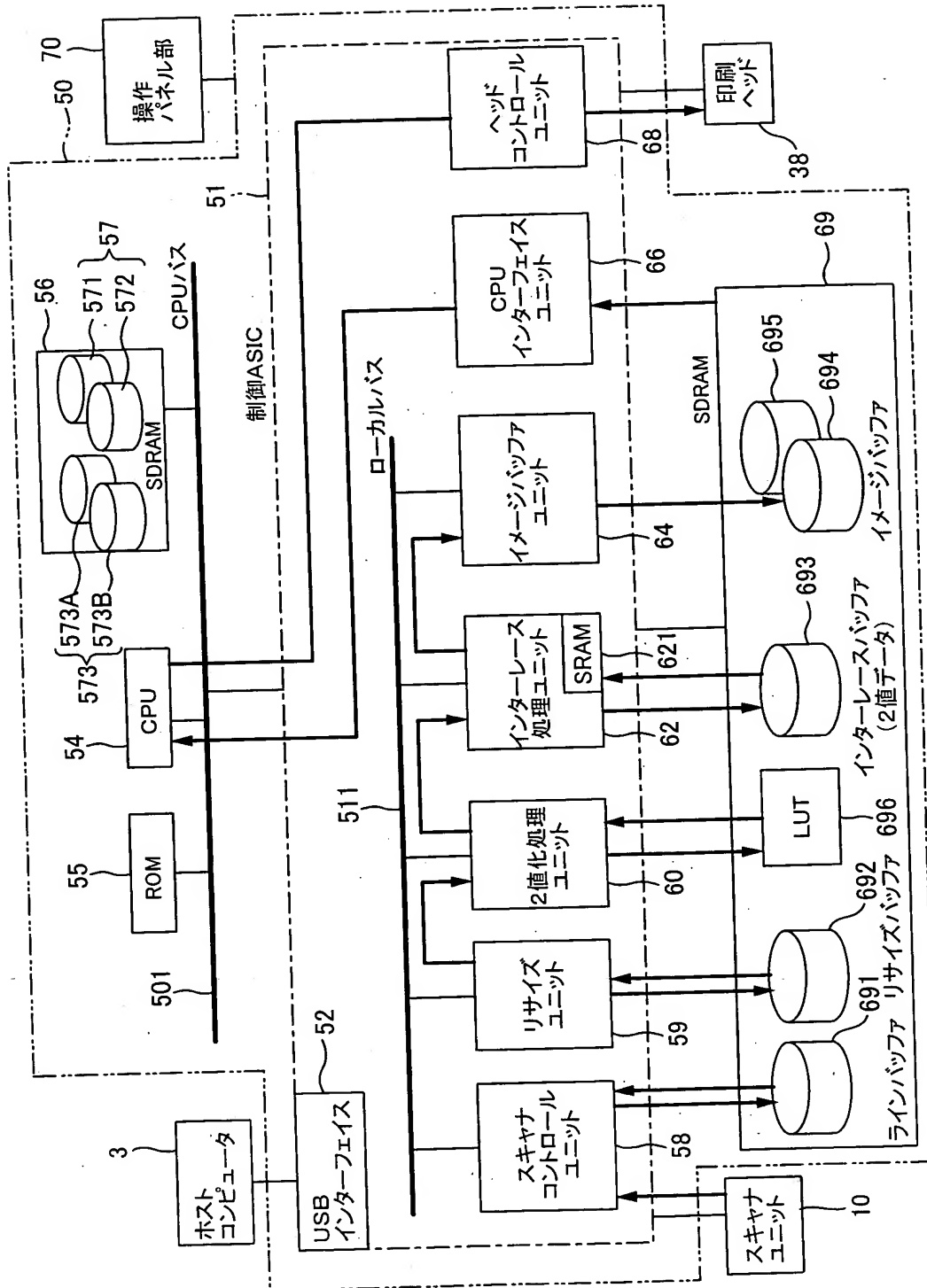


図 26

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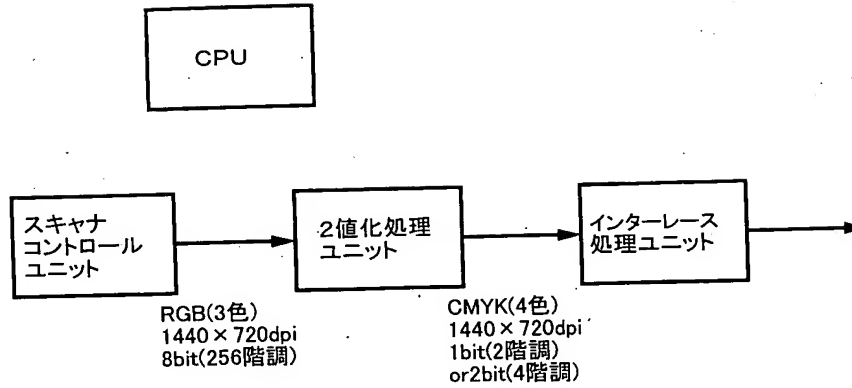


図27A

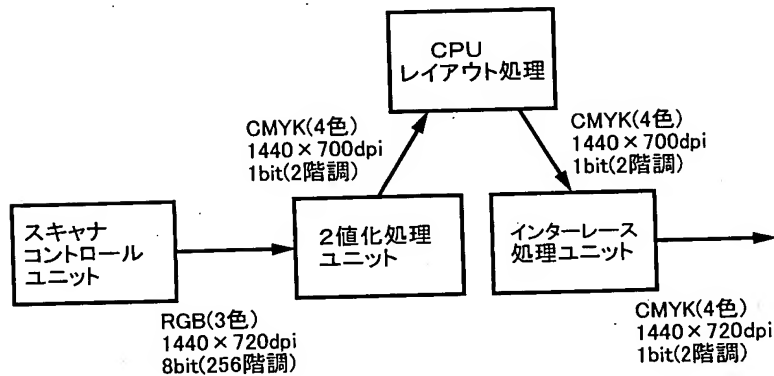


図27B

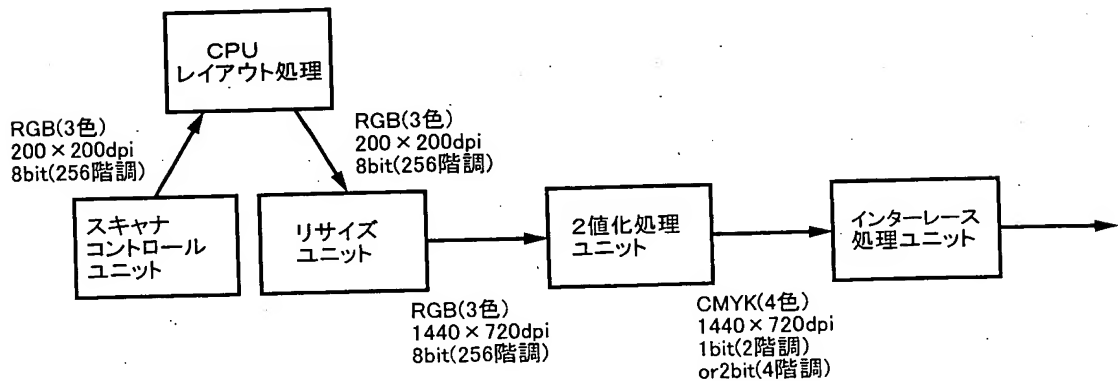
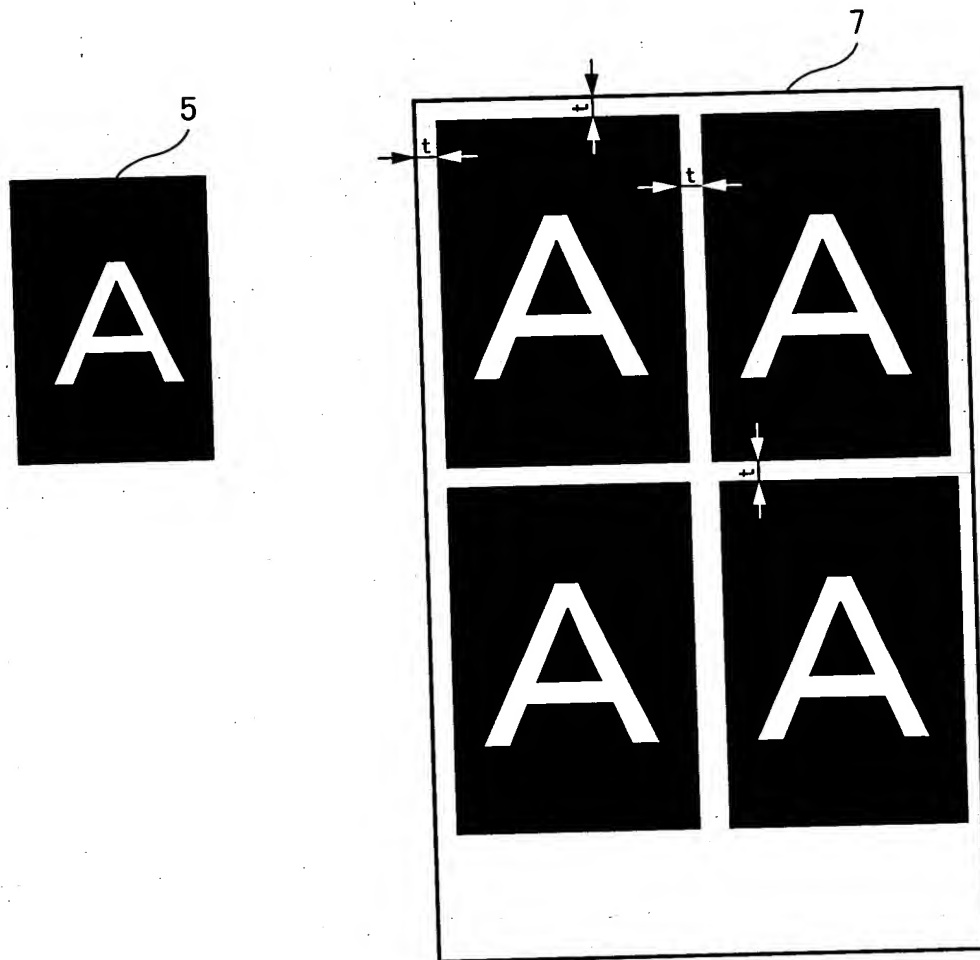


図27C

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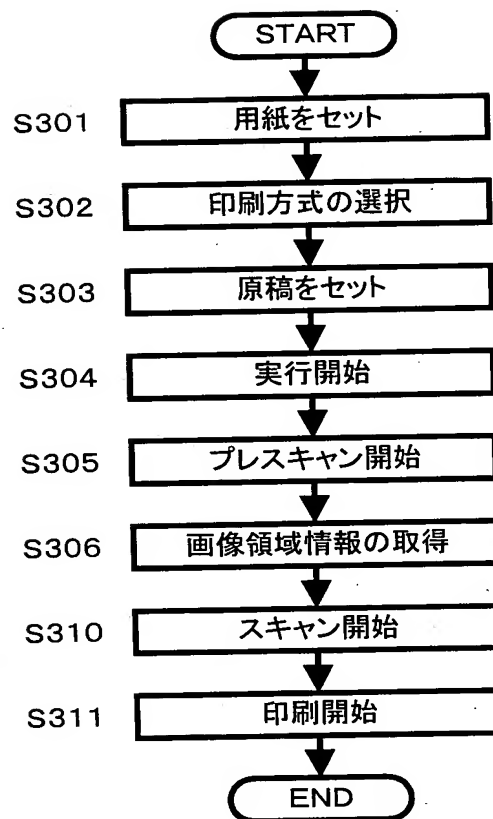


図29

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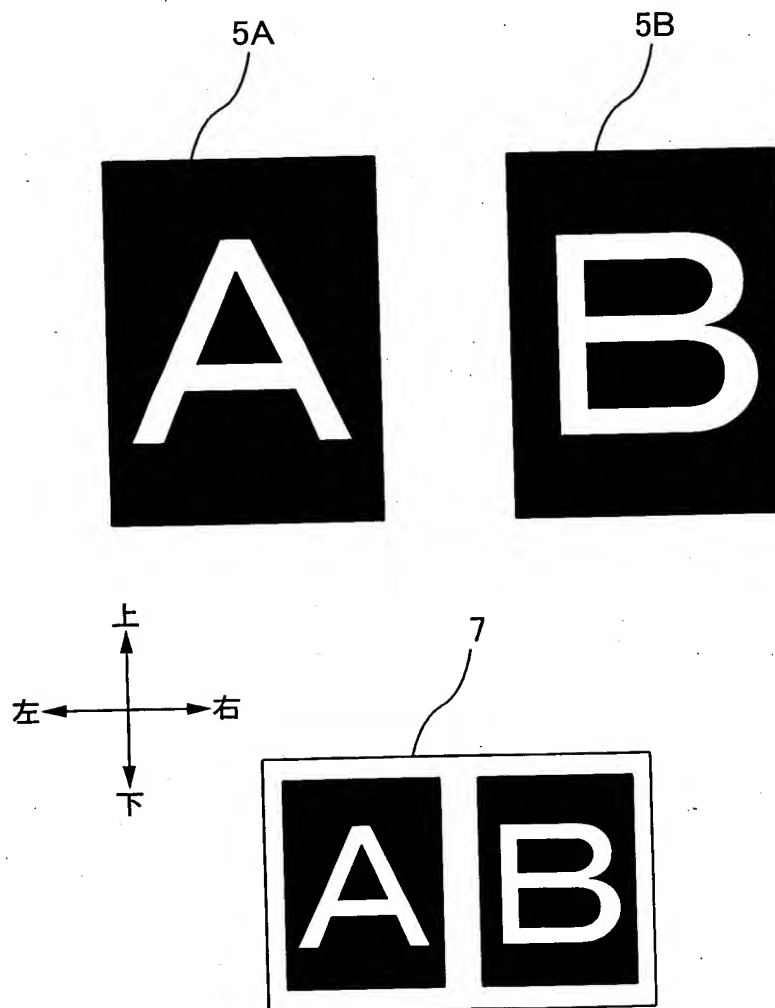


図30

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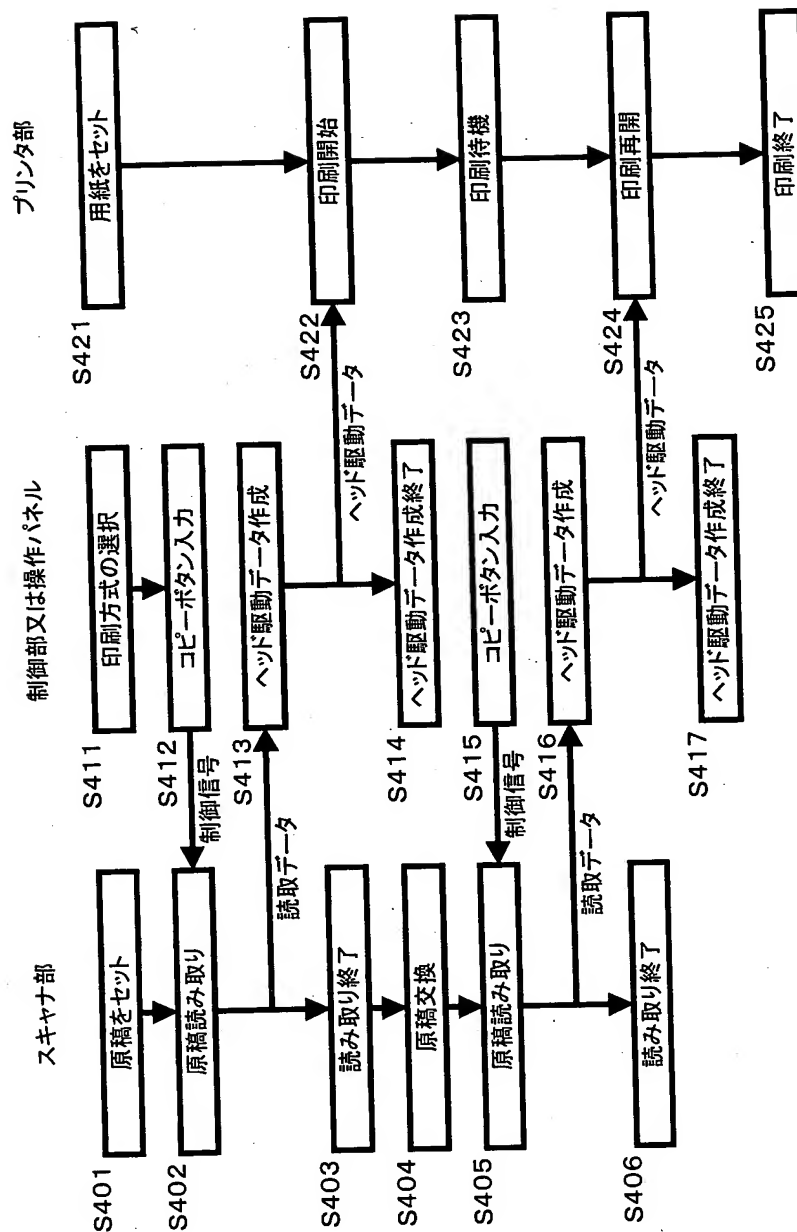


図31

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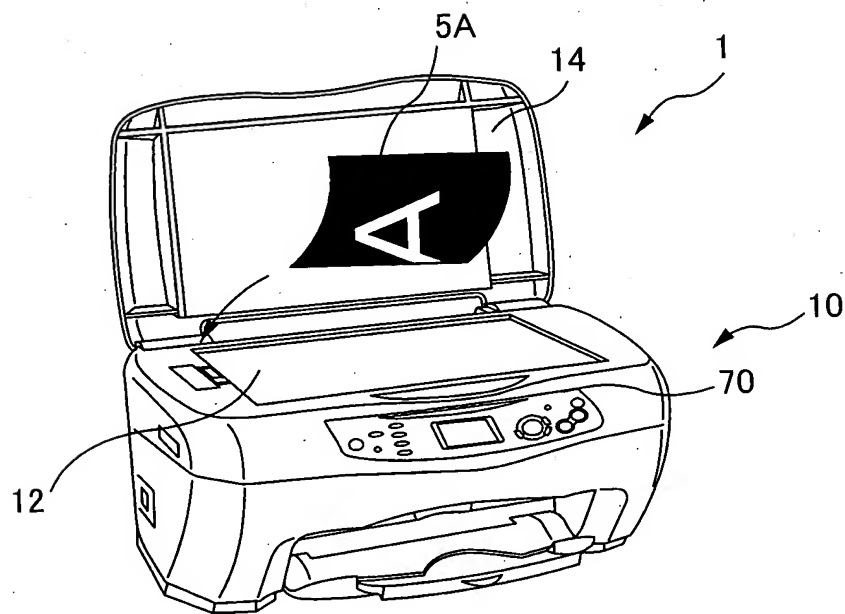


図32A

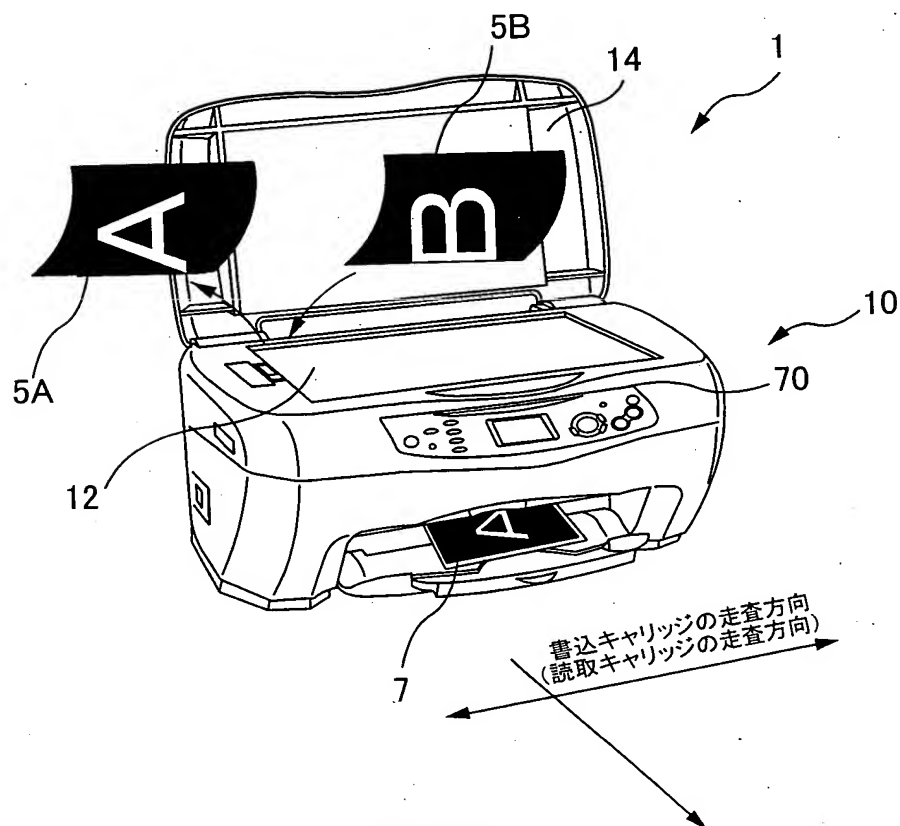
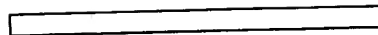


図32B

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☒33A



☒33B



☒33C



☒33D



☒33E

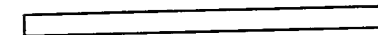


⋮

☒33F



☒33G



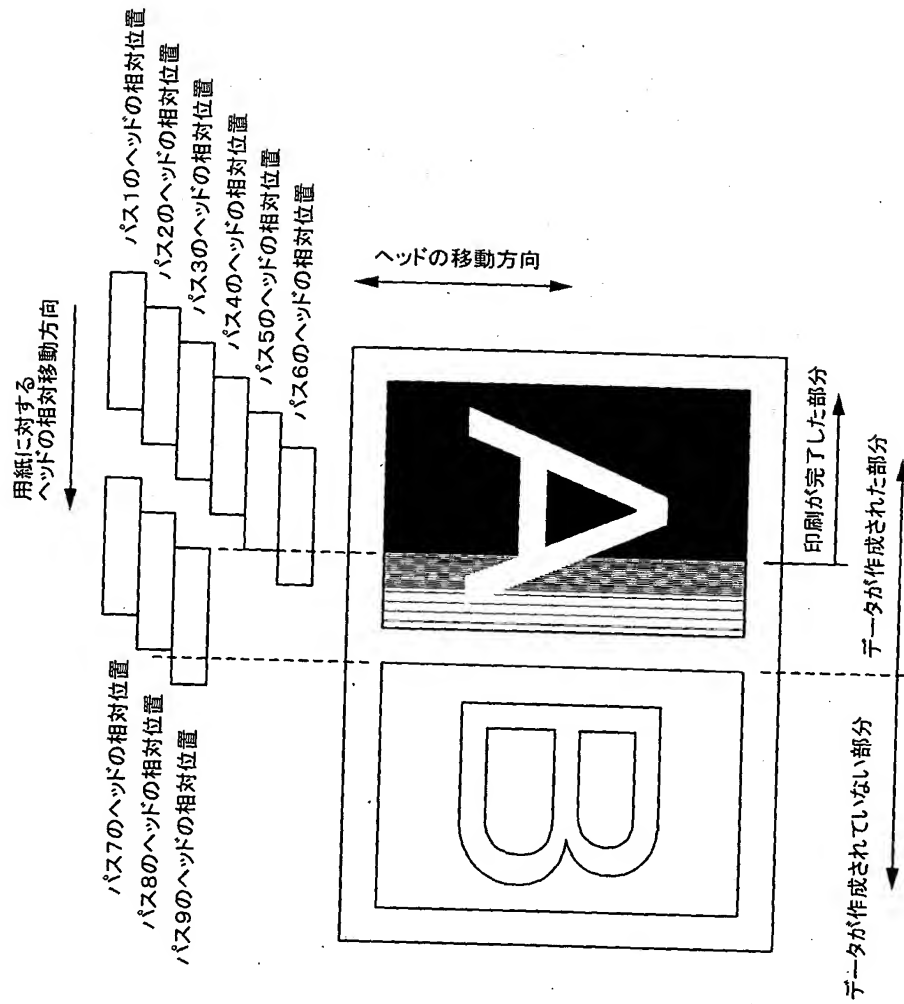


図 34

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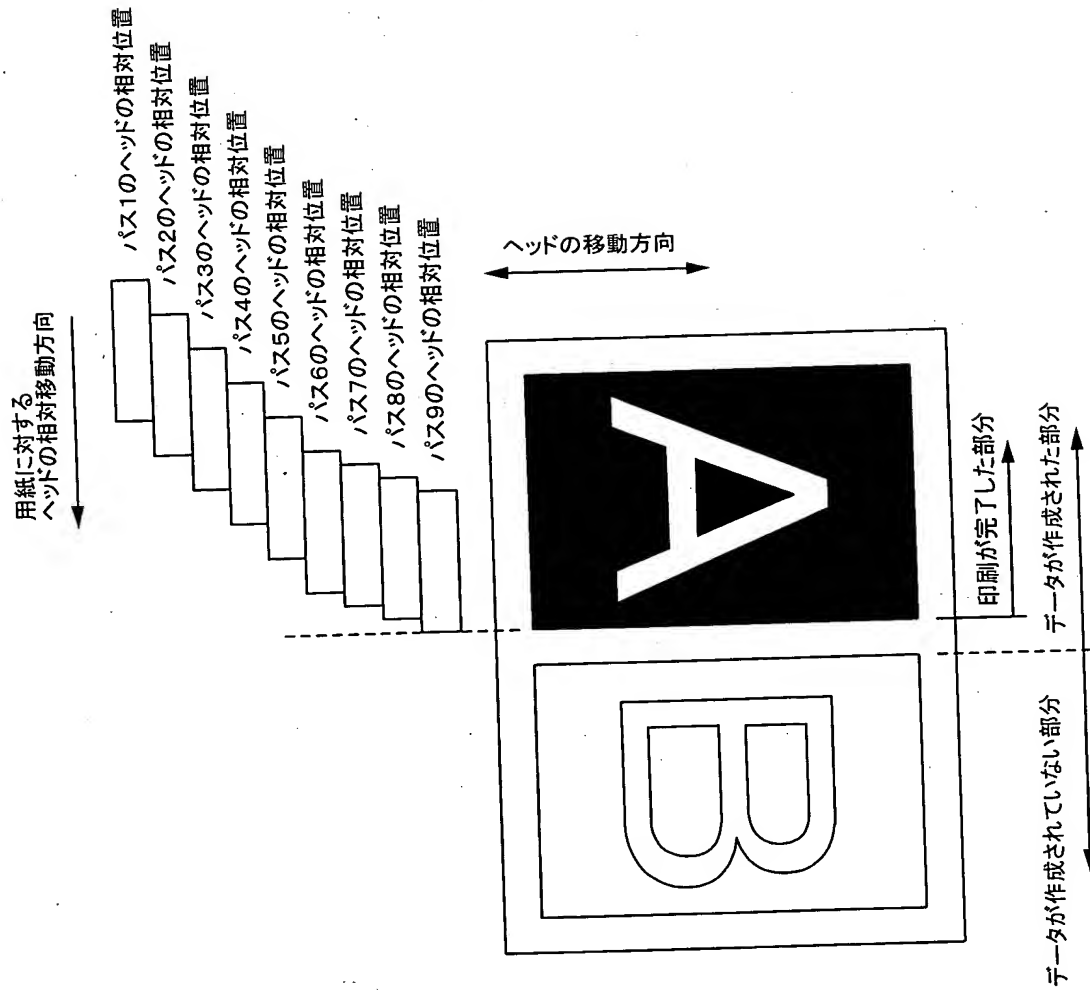


図35

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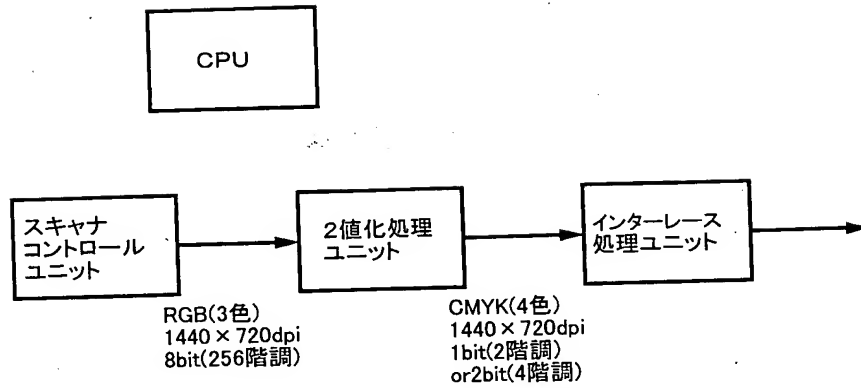


図36A

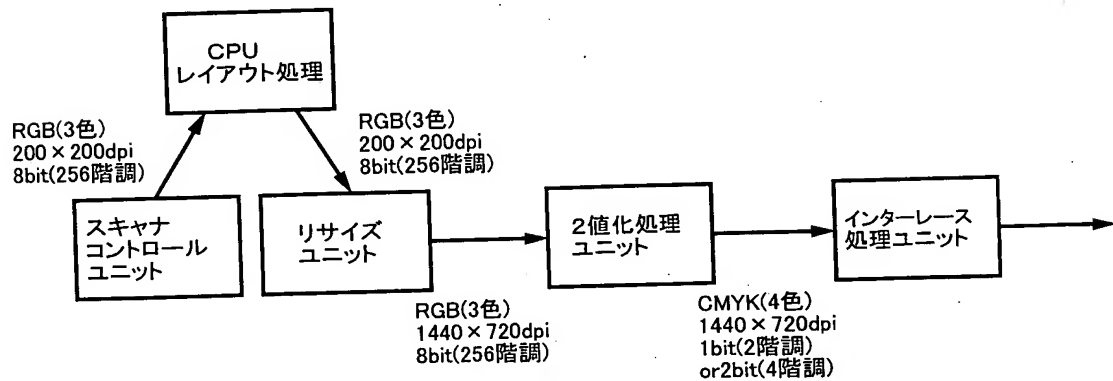


図36B

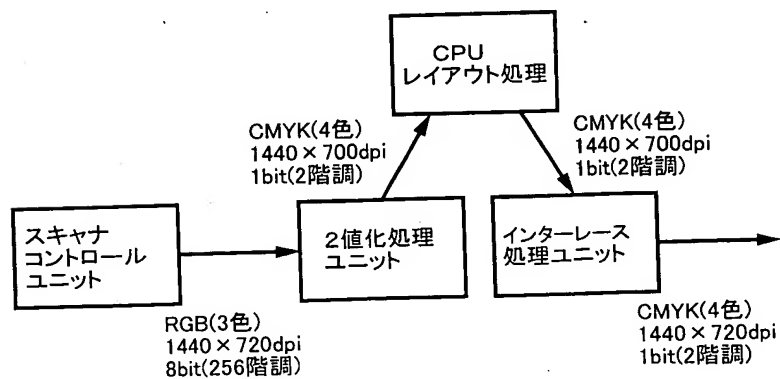


図36C

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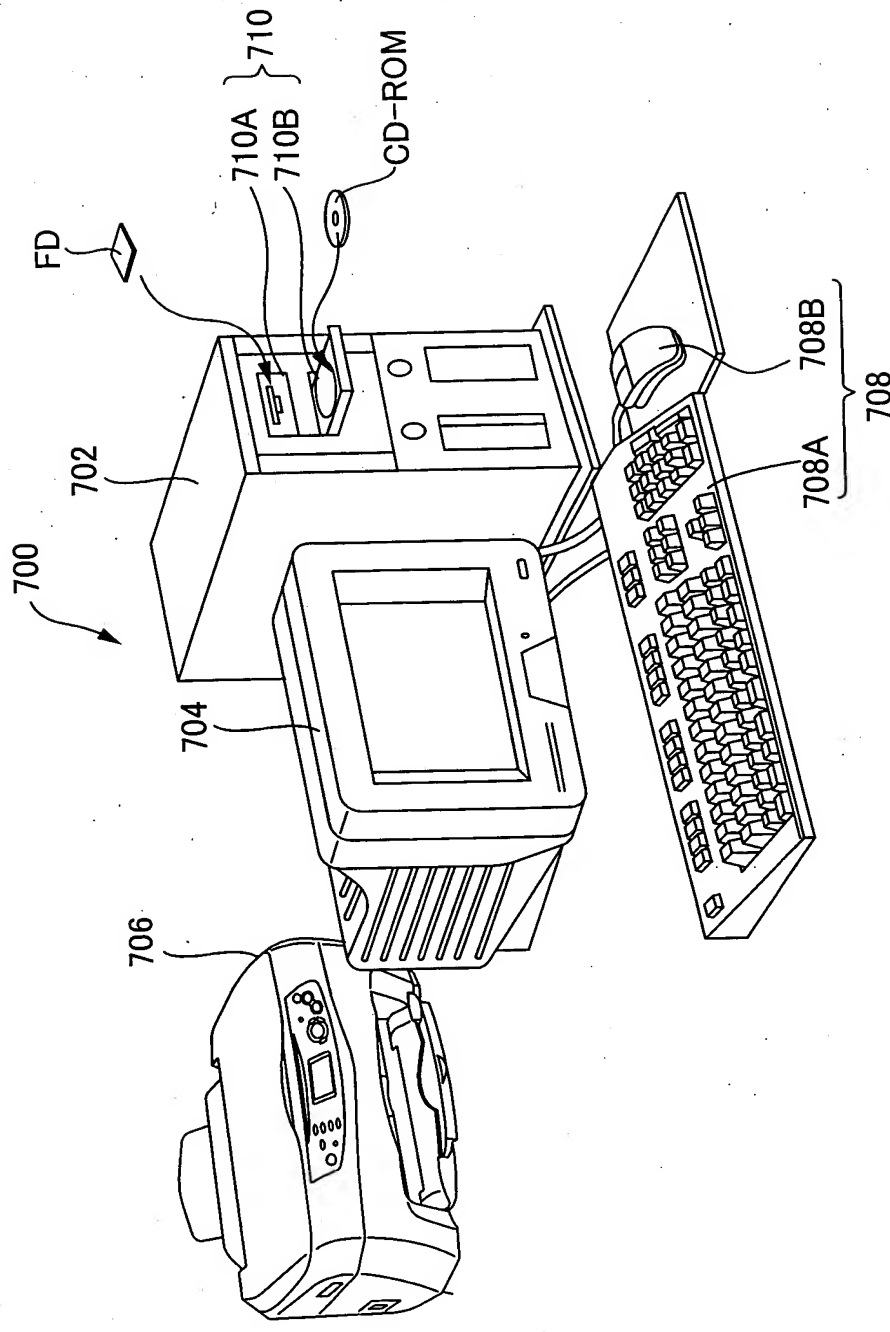


FIG. 37

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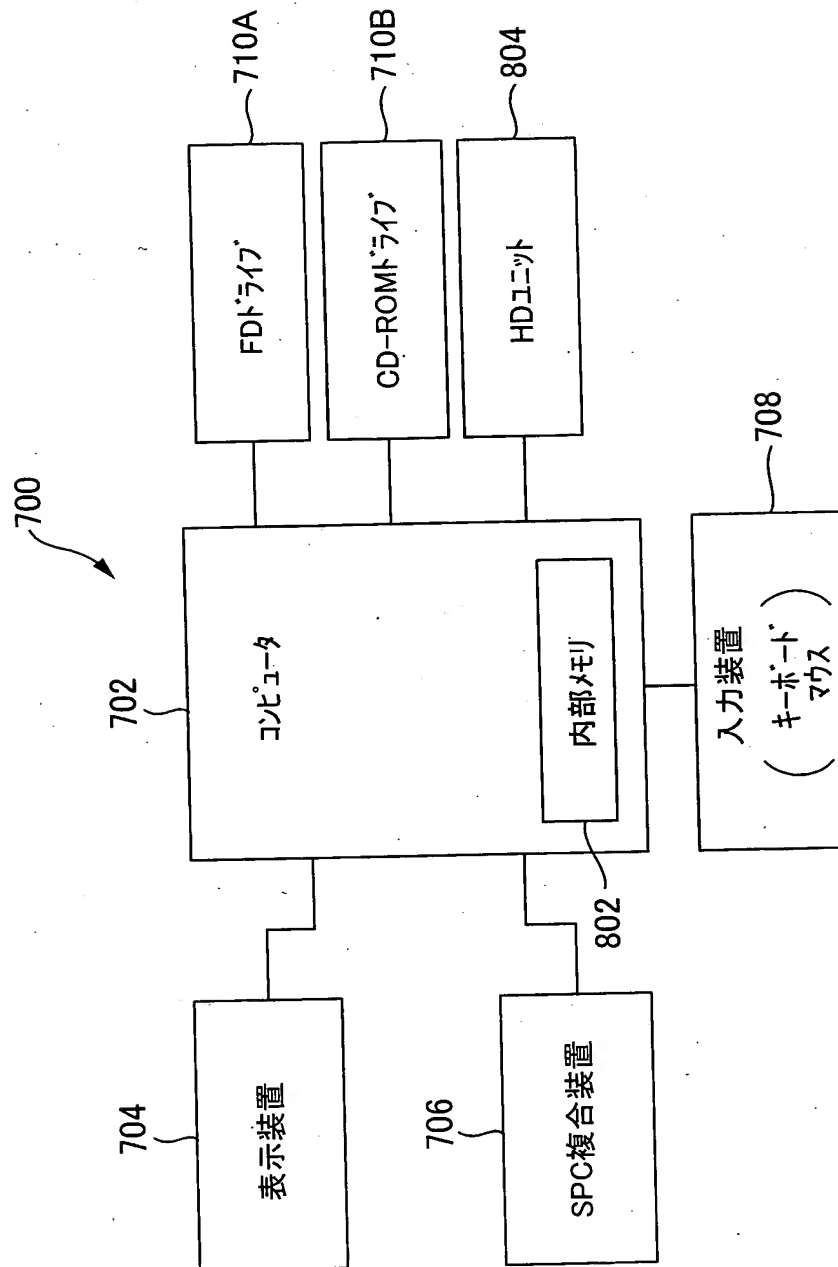


図38

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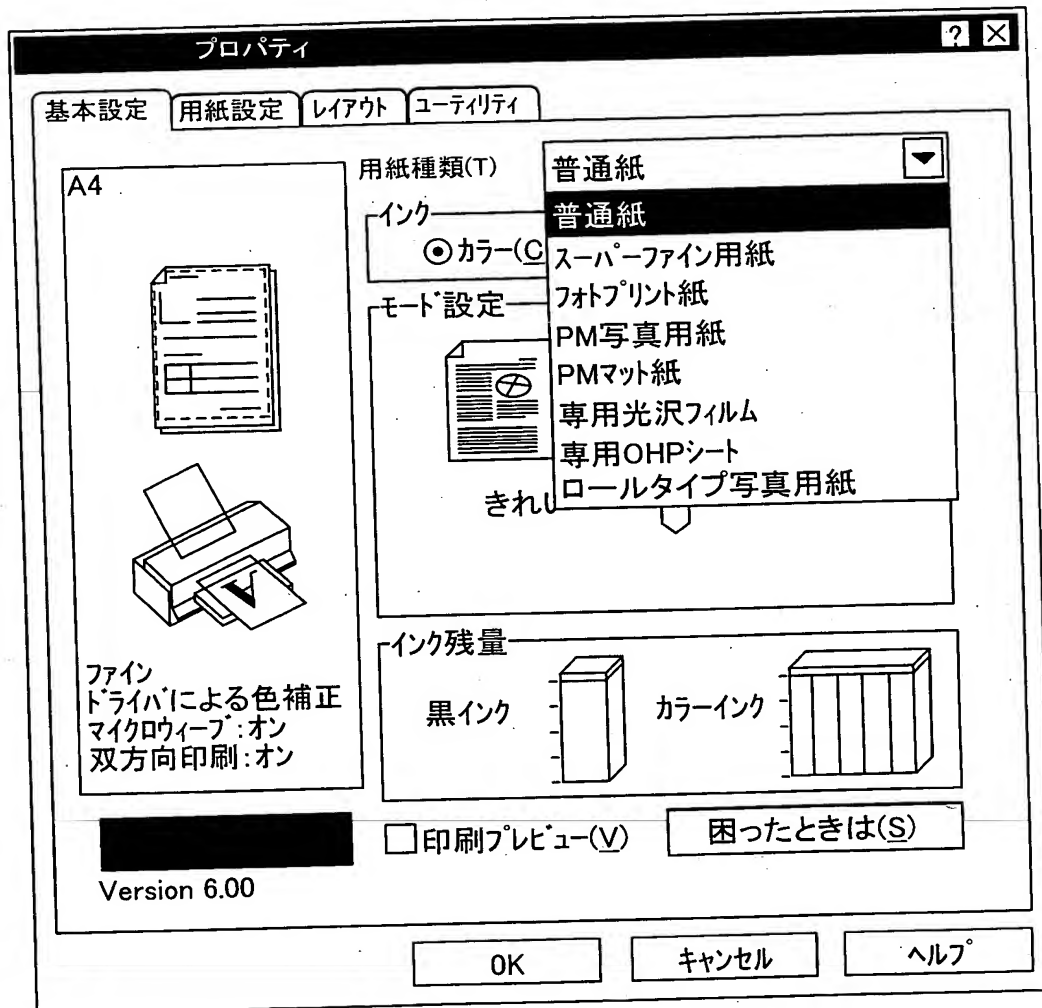


図39

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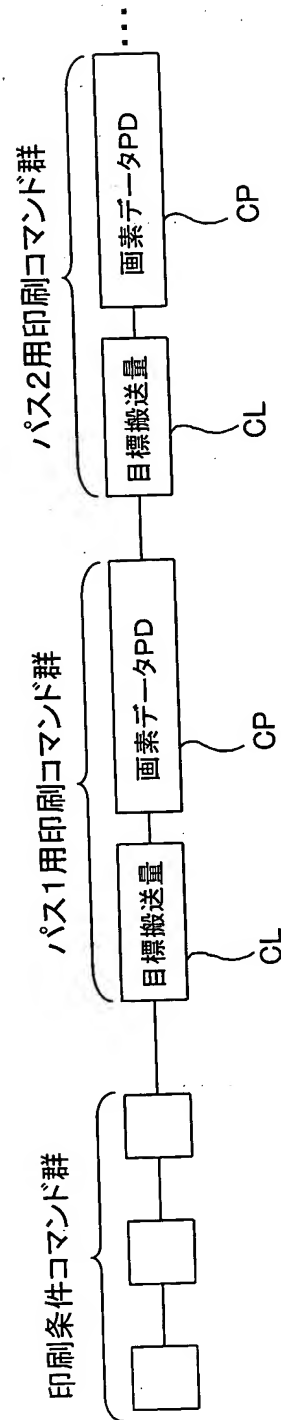


図40